APPENDIX A

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Edward O. Clapper	§ Group Art Unit:	2645
Serial No.:	10/081,819	§	
Filed:	February 22, 2002	§ Examiner:	Olisa Anwah
For:	Providing Information to Facilitate Telephone Conversations	§ Atty. Dkt. No.:	ITL.0694US (P13225)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Declaration under 37 C.F.R. 1.131

I, Timothy N. Trop, declare as follows:

1.) I am a patent attorney with Trop, Pruner & Hu, P.C., 8554 Katy Freeway, Suite 100, Houston, TX 77024, and am qualified to make this declaration based on personal knowledge of the facts declared herein.

2.) An invention disclosure form corresponding to the above-entitled patent application was received in our office on or about October 2, 2001 from Intel Corporation ("Intel"), assignee of the above-entitled application. Attached hereto as Exhibit A is a true and correct copy of this invention disclosure form.

3.) While the dates showing the date of creation and the date the invention disclosure document was received in the Intel legal department have been redacted, these dates are prior to the September 28, 2001 filing date of U.S. Patent Application Publication US 2003/0063732 A1 and the October 10, 2001 publication date of European Patent Application

EP 1143667A2. More so, the invention disclosure form was received with a corresponding Intel data sheet (attached at Exhibit A, p. 4), indicating that the above-entitled application was opened and assigned to Trop, Pruner & Hu, P.C. by Intel on September 28, 2001.

4.) As discussed above, the invention disclosure form of Exhibit A was received in our office on or about October 2, 2001. At the time of receipt, I maintained a reasonable backlog of unrelated patent applications to be written. I worked with a patent agent trainee on this application.

5.) The patent agent trainee prepared many drafts of the specification and claims for the above-entitled application which I reviewed and revised. Attached hereto as Exhibit B is a true and correct copy of a printout from our computer system showing a list of computer files directed to the above-entitled application prepared by the patent agent trainee.

6.) I sent a draft of the patent application to the inventor and the Intel legal department on February 19, 2002, as evidenced by a cover letter accompanying the draft, a true and correct copy of which is attached hereto as Exhibit C.

7.) The inventor reviewed the draft patent application diligently, and executed a declaration for the above-entitled application on February 22, 2002, three days later.

8.) The above-entitled application was filed that same day, February 22, 2002.

9.) Thus the above-entitled application was diligently worked on from the time it was received in our office on or about October 2, 2001 until it was filed on February 22, 2002.

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both under §1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Date: 1/30/04

Timothy N. Trop

P13225
21569

INTEL CONFIDENTIAL

INTEL INVENTION DISCLOSURE

ATTORNEY-CLIENT PRIVILEGED COMMUNICATION

DATE: REDACTED — Connected Products / NCG / HPG

It is important to provide accurate and detailed information on this form. The information will be used to evaluate your invention for possible filing as a patent application. When completed and signed, please return this form to the **Legal Department at JF3-147**. If you have any questions, please call **264-0444**.

1. Inventor CLAPPER (Last Name) EDWARD (First Name) O (Middle Initial)

Phone 480.554.4579 M/S: CH6-302 Fax # 503.210.9227

Citizenship: UNITED STATES WWID 10582543 Contractor: YES ____ NO X

Inventor e-mail Address: edward.o.clapper@intel.com

Home Address: 101 EAST RIVIERA DRIVE

City TEMPE State: AZ Zip 85226 Country UNITED STATES

*Corporate Level Group (e.g. iABG, NCG, CEG): NBG Division: HPG Subdivision: HW ENG

***If you are unsure of this information, please discuss with your manager.**
(PROVIDE SAME INFORMATION AS ABOVE FOR EACH ADDITIONAL INVENTOR)

2. Title of Invention: Extended PC - Rolodex Peripheral

3. What technology/product/process (code name) does it relate to (be specific if you can):
information appliances, extended PC, connected products

4. Include several key words to describe the technology area of the invention in addition to # 3 above:
address, caller ID, phone, module, hardware, peripheral, Internet, communicate, phone number, store, retrieve

5. Stage of development (i.e. % complete, simulations done, test chips if any, etc.)
0%

6. (a) Has a description of your invention been, or will it shortly be, published outside Intel:

NO X YES: ____ If YES, was the manuscript submitted for pre-publication approval?

IDENTIFY THE PUBLICATION AND THE DATE PUBLISHED:

(b) Has your invention been used/sold or planned to be used/sold by Intel or others?

NO: X YES: ____ DATE WAS OR WILL BE SOLD:

(c) Does this invention relate to technology that is or will be covered by a SIG (special interest group)/standard/ or specification?

NO: X YES: ____ Name of SIG/Standard/Specification:

(d) If the invention is embodied in a semiconductor device, actual or anticipated date of tapeout?

(e) If the invention is software, actual or anticipated date of any beta tests outside Intel

7. Was the invention conceived or constructed in collaboration with anyone other than an Intel blue badge employee or in performance of a project involving entities other than Intel, e.g. government, other companies, universities or consortia? NO: X YES: ____ Name of individual or entity:

8. Is this invention related to any other invention disclosure that you have recently submitted? If so, please give the title and inventors:

DESCRIPTION: Extended PC - Rolodex Peripheral

This disclosure is a proposal for extending the PC's presence even when the PC is not in use or powered on. While some people keep contact data in their computer via various applications, it is often necessary to have phone numbers, addresses, etc., handy in household locations that are remote from the computer, or at times when the computer is not activated. Most people needing a phone number will not fire up their computer to get it. As a result, people still resort to the old-fashioned Rolodex®, or similar means – even the simple (but not very elegant) solution of keeping a hard copy printout of the common names, numbers and addresses one might need.

The proposed device extends the PC's management power for such data, while freeing the user from actually requiring the PC during inconvenient times; it also frees the user from needing hardcopies or phone books for people in their contact listing. Lastly, some embodiments may be envisioned where the product is actually hardware supported by a service. In this case, a phone company (for example) may supply complete address, email, names, numbers and even information such as birthdays, etc. as a record in the device at the user's request. All that is required is for the user to make a call or receive one to/from the contact in question. The service does the rest – while you are on the phone! I think that a possible name for it might be "*i.Dex*".

Refer to Figures for the following feature list and usage model:

1. Technology:
   - Caller ID
   - Communications relationship with a PC (wires or wireless)
   - Local data storage with synchronization capability to a PC application over local connection.
   - Some embodiments may include a printing module (technology may vary), to produce labels.
2. Claim-centric concepts:
   - An apparatus comprised of a processor, memory, power supply, display, user interface, telephone interface, and computer connection interface. The apparatus contains independent means of locally storing data indefinitely, as well as local controls for manipulating the data.
   - A means by which incoming CallerID data may be stored in the device for upload to a PC or processing system.
   - A means by which dialed telephone numbers may be captured by the device and stored for upload to a PC or processing system.
   - A means by which the device may communicate with a PC or a network in order to exchange data.
   - A software application that manages the upload and download of data between the device and a processor-based system or network.
   - A PC-resident application that retrieves extended information from a data source such as the Internet, using seed data from the device; examples of seed data include CallerID name and number, and numbers dialed from the device residence. Examples of extended data include address, publicly available personal data, inferred email addresses, etc. found to be relevantly associated with the seed data.
   - A means by which the extended data and seed data may be formatted, either in the PC application, the device interface or both.
   - A means by which the full data record (extended data, seed data, and possibly call logging) may be manipulated by the user, affecting the data record resident in the PC, the device, or both.
3. Advantages over current methods and benefits to Intel:
   - May be sold independent of a PC, and used to stimulate interest in PC advantages for consumers who have not yet purchased a computer.
   - Operates stand-alone; database is synchronized to a PC as available, but is not necessary in order to be valuable. Most connected devices in this type of category do not have functionality when the PC is powered down. This device is self-contained, with connectivity as a distinct advantage.
   - May be packaged with phone services, providing extended information automatically to the device over data or Caller ID protocols.
   - Intel is promoting the concept of the "Extended PC". This device is a very useful example of how the PC may be extended within the home.
   - Development of such a product should be fairly inexpensive, as it is specifically featured for limited functionality.

4. Prior art:
   - None observed.
5. Infringement detection and partnership options:
   - Partnerships with Rolodex®, or other brands in this space would be valuable.
   - Infringement may be detected by determining if the device in question is able to query a phone system and/or computer for remotely stored contact data. In addition, the capability to independently store and manipulate such data in the device itself is part of the configuration's advantages.

Figure 1 – POTENTIAL EMBODIMENT

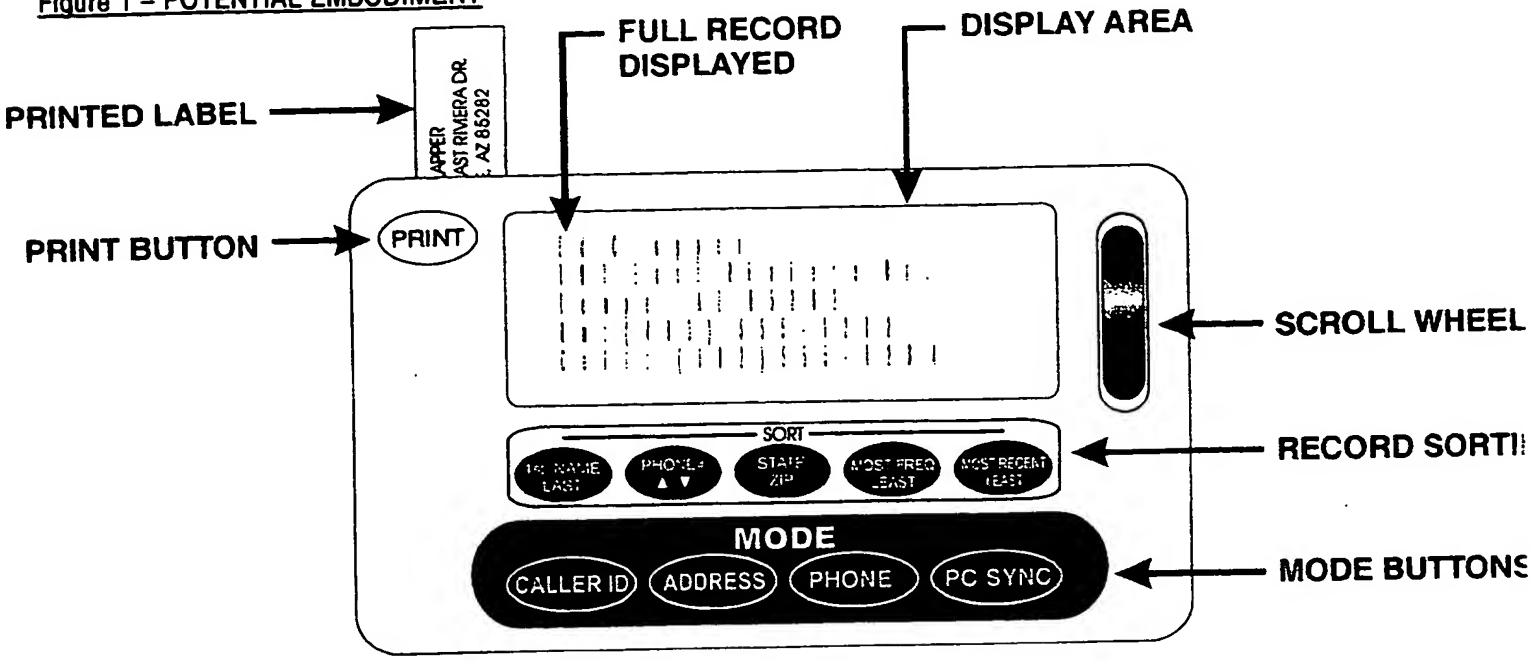
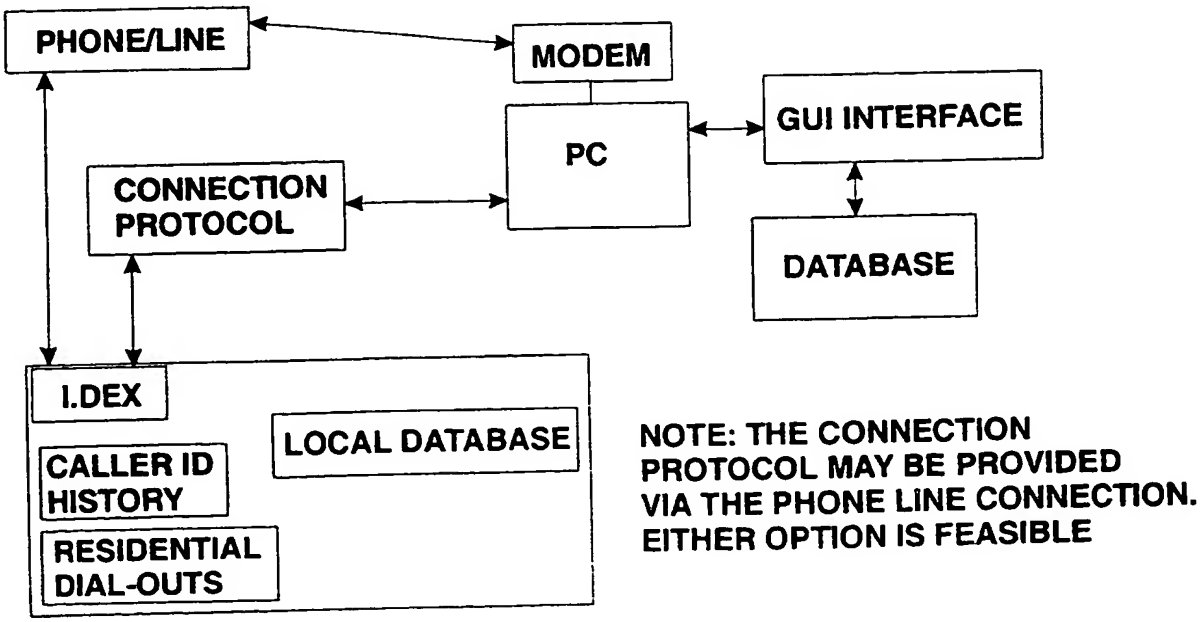


Figure 2 – BASIC SYSTEM DIAGRAM



PLEASE READ AND FOLLOW THE DIRECTIONS ON HOW TO WRITE A DESCRIPTION OF YOUR INVENTION

Please attach a description of the invention to this form, DATED AND SIGNED BY AT LEAST ONE PERSON WHO IS NOT A NAMED INVENTOR, and include the following information:

1. **Describe in detail what the components of the invention are and how the invention works.**
2. **Describe advantage(s) of your invention over what is done now.**
3. **YOU MUST include at least one figure illustrating the invention. If the invention relates to software, include a flowchart or pseudo-code representation of the algorithm.**
4. **Value of your invention to Intel (how will it be used?).**
5. **Explain how your invention is novel. If the technology itself is not new, explain what makes it different.**
6. **Identify the closest or most pertinent prior art that you are aware of.**
7. **Who is likely to want to use this invention or infringe the patent if one is obtained and how would infringement be detected?**

***HAVE YOUR SUPERVISOR READ, DATE AND SIGN COMPLETED FORM**

DATE: _____ SUPERVISOR: _____

BY THIS SIGNING, I (SUPERVISOR) ACKNOWLEDGE THAT I HAVE READ AND UNDERSTAND THIS DISCLOSURE, AND RECOMMEND THAT THE HONORARIUM BE PAID

Matter #: **P13225** Intel Grp Atty: **HA[redacted] EL** Work Atty: **TPH**

Matter Status: **IN PROCESS**

TYPE OF INTEL PATENT APPLICATION FILE

*Patent: ❑ **Utility** ❑ **Design** ❑ **Reissue** ❑ **Reexam** ❑ **CPA (C)** ❑ **CIP (X)** ❑ **Divisional (D)**

Title of File: **EXTENDED PC - ROLODEX PERIPHERAL**

INTEL DISCLOSURE AND FOREIGN FILING INFORMATION

*Disclosure number(s): **21569**

*Product/Process: **INFORMATION APPLIANCES, EXTENDED PC, CONNECTED PRODUCTS**

Intel Committee: **CONNECTED PRODUCTS** Intel Group: **NCG** Intel Division: **HPG**

F-Filing: **SELECTED** Ctry: **TW; PCT; JP; KR; GB; DE; HK; CN; NL; DK**

Notes: **P13225 (21569) OPENED & ASSIGNED TO TPH PER JB EMAIL 9/28/01 AL.**

*INTEL ABSTRACT CODES (Check One or More)

- __PROCESS (C1)
  - __N or P MOS (C1A)
  - __Equipment (C1B)
  - __CMOS (C1C)
  - __Contacts (C1D)
  - __Flash (C1E)
  - __GaAs and SOS (C1F)
  - __Circuit element (C1G)
  - __Isolation/Insulation (C1H)
  - __BiCMOS (C1I)
  - __Analysis/Testing (C1J)
  - __Etching/Planarization (C1K)
  - __Metal (C1L)
  - __Poly silicon (C1M)
  - __Passivation (C1N)
  - __Masking/Resist (C1O)
  - __Deposition (C1P)
  - __Implantation (C1Q)
- __DRAMs (C2)
  - __Sense amp (C2A)
- __SRAMs (C3)
  - __Sense amp (C3A)
- __EPROMS (C4)
  - __P-channel (C4A)
  - __N-channel (C4B)
  - __Flash (C4C)
  - __EE (C4D)
  - __Sense amp (C4E)
  - __Solid-State disk (C4F)
  - __Flash Card (PCMCIA) (C4G)
  - __Multibit Cell (C4H)
  - __Redundancy (C4I)
  - __Blocking (C4J)
  - __Write Automation (C4K)
  - __Minicard (C4L)
  - __ Camera (C4M)
  - __ FMM (C4N)
  - __ Firmware Hub (FWH) (C4O)
  - __ Security (C4P)
  - __ Small Block (C4Q)
  - __ FDI (C4R)
  - __ Interface (C4S)
  - __ Connector (C4T)
  - __ Cell Phone (C4U)
  - __ Charge Pump (C4V)
  - __ Audio (C4W)
- __ Microprocessor (C5)
  - __ Embedded (C5A)
  - __Buses Input/Output Devices (C5B)
  - __Protocol/CPU Interfacing (C5C)
  - __Adder/Multiplier Units (C5D)
  - __Numeric (C5E)
  - __Video/Graphics (C5F)
  - __Cache/memory Hierachy/ Memory/Virtual Memory (C5G)
  - __Memory Management/ Protection/Addressing (C5H)
  - __Instruction/Inst. Decoding/ Microcoding/Sequencing/ Microprogrammed Control (C5I)
  - __Pipeline/Parallelism (C5J)
  - __Clocking/Clock Generation/ Clock Multiplication (C5K)
  - __Addressing/Addressing Modes (C5L)
  - __Vector Processing (C5M)
  - __Registers/Files/Stacks (C5N)
  - __Multiprocessing/Dual (C5O)
  - __Initialization/Testing/ Debugging (C5P)
  - __Program/Program Control/ Interrupt/Status/Faults Exceptions (C5Q)
  - __RISC (C5R)
  - __Redundancy (C5S)
- __SYSTEMS (C6)
  - __Bus (C6A)
  - __Supercomputers (parallel multiprocessors) (C6B)
  - __Compilers (C6C)
  - __Test Equipment (ICE) (C6D)
  - __BIOS (C6E)
  - __PCMCIA (thin removable functionality cards, i.e., memory, modem, network, etc.) (C6F)
- __Magnetics (bubble memories) (C7)
- __Buffers (C8)
- __Packaging/Mounting/ Connector (C9)
- __Logic (C10)
- __Neural (C11)
- __Miscellaneous (C12)
- __General Memories (C13)
  - __Redundancy (C13A)
  - __Rambus-compatible (C13B)
  - __General Circuit (C14)
  - __Peripherals (C15)
  - __ROM (C16)
  - __Timing Clocks (C17)
  - __Power/Regulation (C18)
  - __Networks (C19)
  - __PLD (C20)
  - __Compression/Decompression (C21)
- __Video/Graphics/Audio (C22)
  - __ Algorithm (C22A)
  - __ System (C22B)
  - __ Sensor (C22C)
  - __ Optics (C22D)
  - __ 3D (C22E)
  - __ Display (C22F)
  - __ Graphics Device (C22G)
- __Test Equipment (C23)
- __Video Teleconferencing (C24)
- __Communication (C25)
- __ Software (C26)
  - __ Graphics (C26A)
  - __ Audio (C26B)
  - __ Compiler (C26C)
  - __ Operating System (C26D)
  - __ Drivers (C26E)
  - __ Other (C26F)
- __ IAL (C27)
  - __ Internet/WWW Applications (C27A)
  - __ Java Applics. (C27B)
  - ✓ User Interfaces Consumer (C27C)
  - __ Appliances Portable (C27D)
  - __ Computing (C27E)
- __ Compilers (C28)
  - __ Java Compilers (C28A)
  - __ Java Just-in-Time (C28B)
  - __ IA64 Compilers (C28C)
  - __ Optimization (C28D)
- __ Circuits (C29)
  - __ New Logic Family (C29A)
  - __ Data Path (C29B)
- __ Chipsets (C30)
  - __ Memory Control (C30A)
  - __ Bridging (C30B)
  - __ Firmware Hub (C30C)
- __ Design Tools (C31)
  - __ Circuits (C31A)
  - __ Layout (C31B)
  - __ Logic (C31C)
  - __ Validation/Test (C31D)
  - __ Low Power (C31E)

continued next page...

***Mandatory for original patent application. File will not be opened unless mandatory information is provided.**

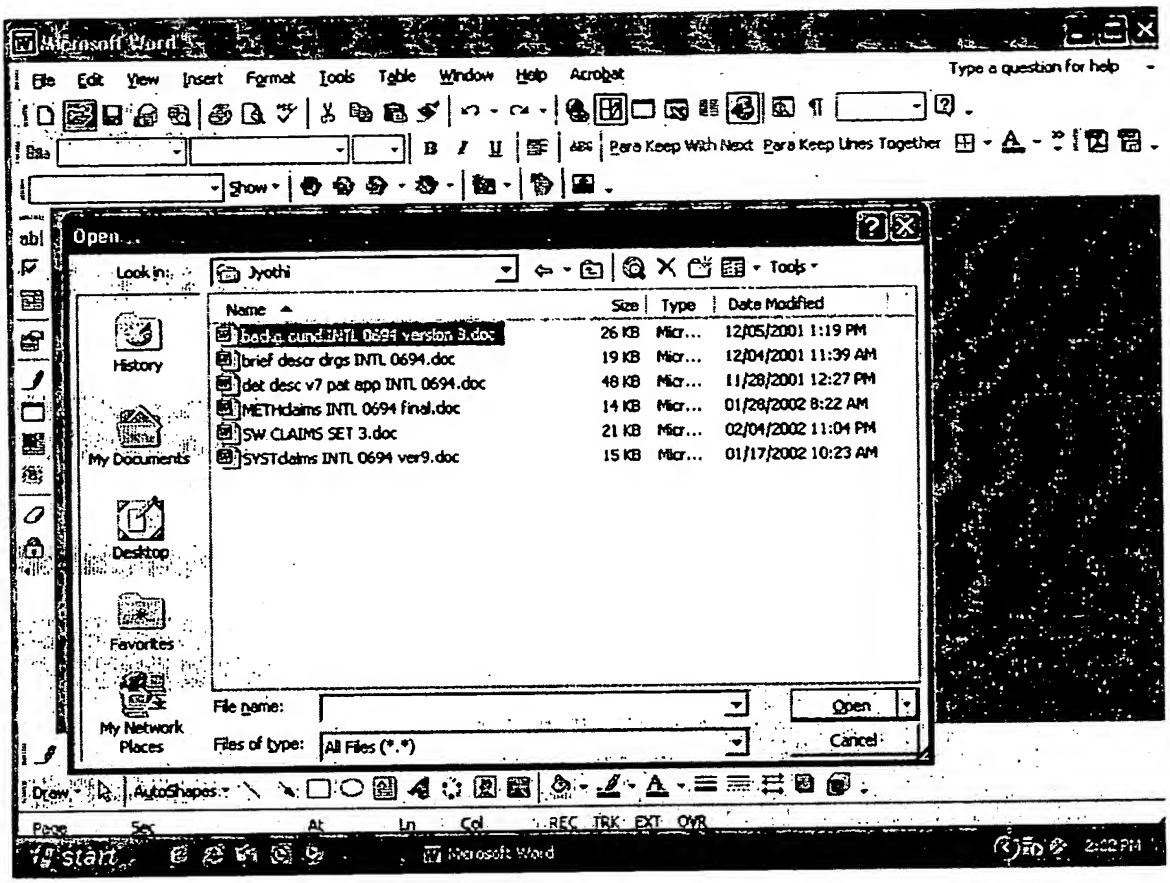

Microsoft Word
File Edit View Insert Format Tools Table Window Help Acrobat
Type a question for help
Para Keep With Next Para Keep Lines Together
Show
Open...
Look in: Jyothi
Tools
Name
Size
Type
Date Modified
background INTL 0694 version 3.doc 26 KB Micr... 12/05/2001 1:19 PM
brief descr drgs INTL 0694.doc 19 KB Micr... 12/04/2001 11:39 AM
det desc v7 pat app INTL 0694.doc 48 KB Micr... 11/28/2001 12:27 PM
METHclaims INTL 0694 final.doc 14 KB Micr... 01/28/2002 8:22 AM
SW CLAIMS SET 3.doc 21 KB Micr... 02/04/2002 11:04 PM
SYSTclaims INTL 0694 ver9.doc 15 KB Micr... 01/17/2002 10:23 AM
History
My Documents
Desktop
Favorites
My Network Places
File name:
Files of type: All Files (*.*)
Open
Cancel
Draw
AutoShapes
REC TRK EXT OVR
start
Microsoft Word
2:02 PM

Exhibit C

TROP, PRUNER & HU, P.C.

INTELLECTUAL PROPERTY LAW ATTORNEYS

8554 Katy Freeway, Suite 100
Houston, Texas 77024

Business: (713) 468-8880
Facsimile: (713) 468-8883

February 19, 2002

VIA UPS

Ms. Janice Boulden
Intel Corporation
2111 NE 25th Street, M/S #JF3-147
Hillsboro, Oregon 97124

Re: U.S. Patent Application
Entitled *Providing Information To Facilitate Telephone Conversations*
Intel's ref: P13225
Our ref: ITL.0694US

Dear Janice:

Enclosed for quality review are the IPQRC form, the ten point checklist, a copy of the original disclosure and a draft of the above-referenced patent application.

If you have any questions or comments, please give me a call.

Very truly yours,

Timothy N. Trop

TNT:lo
Enclosures
IPQRC Form
10 Pt. Check List
Original Disclosure
Draft Patent Application